

Applicant:	Egerer	Docket No.:	QIM 2003 P 50294 US
Serial No.:	10/812,395	Art Unit:	2836
Filed:	March 30, 2004	Examiner:	Michael Rutland-Wallis
For:	Semiconductor Device Voltage Supply for a System with at least Two, Especially Stacked, Semiconductor Devices		

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	Filing Date	March 30, 2004
	First Named Inventor	Egerer
	Art Unit	2836
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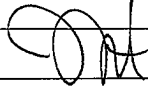
☐ Applicant/Inventor

☐ Assignee of record of the entire interest.
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).

☒ Attorney or agent of record. Registration number 35,272

☐ Registered practitioner named in the application transmittal letter in an application without an executed oath or declaration. See 37 CFR 1.33(a)(1). Registration Number _____

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 Typed or Printed
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